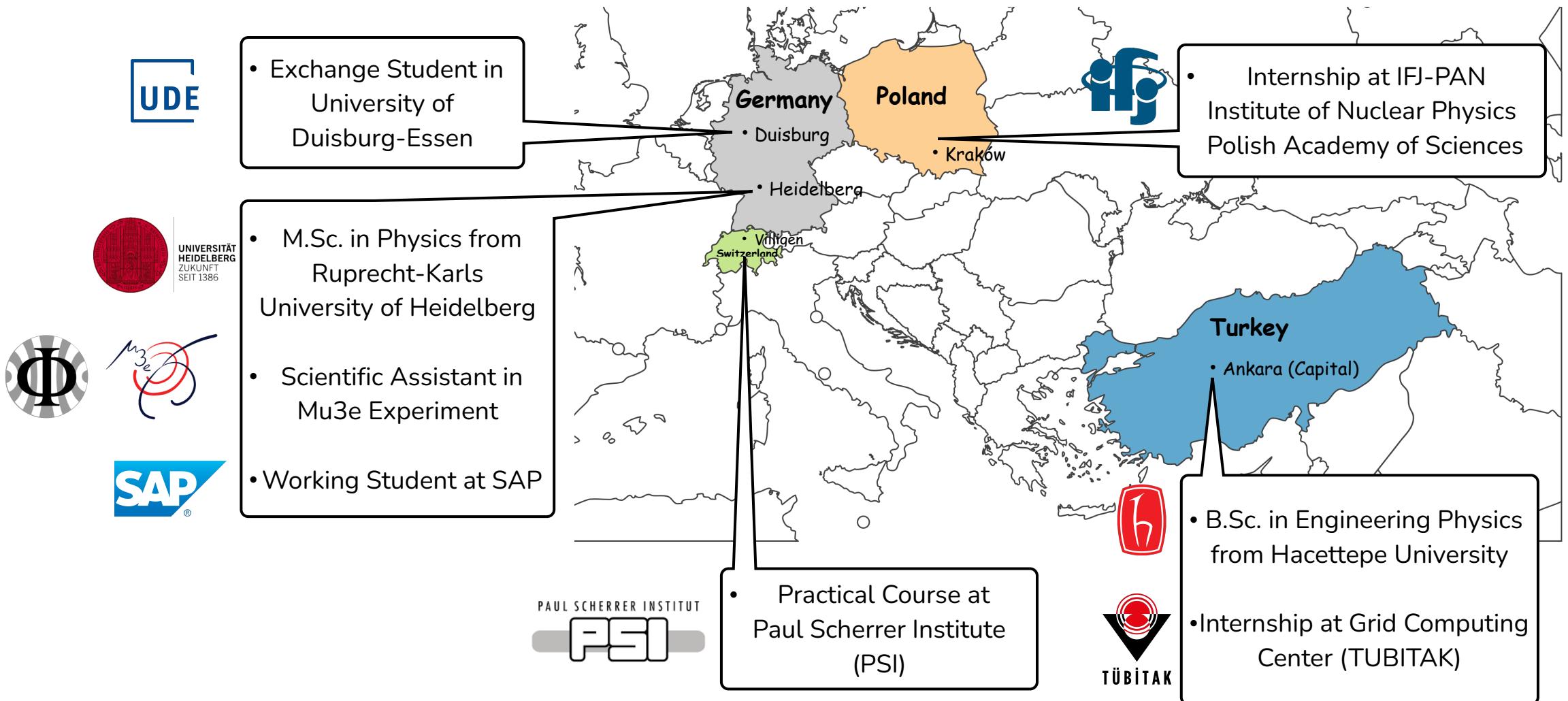

KIT - CMS Experiment

PhD Interview Presentation

12.02.2026

Kadir Tastepe

Kadir's Academic Journey



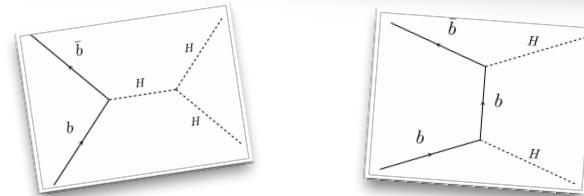
Undergraduate Experience

Bachelor Project: 

Motivation: The measurement of Higgs self-coupling represents one of the most important remaining tests of the Standard Model's electroweak sector.

Higgs pair production $b\bar{b} \rightarrow HH$ at 14 TeV using Monte Carlo simulation tools has been studied, and the cross-section has been simulated.

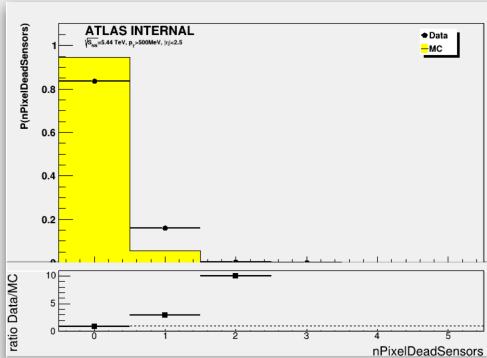
End-to-end Monte Carlo Simulation chain has been created.



- Realistic Kinematic distributions are obtained.
- The lepton universality has been validated in the simulations.
- Baseline chain has been created for future analysis.

Internship at Polish Academy of Sciences: 

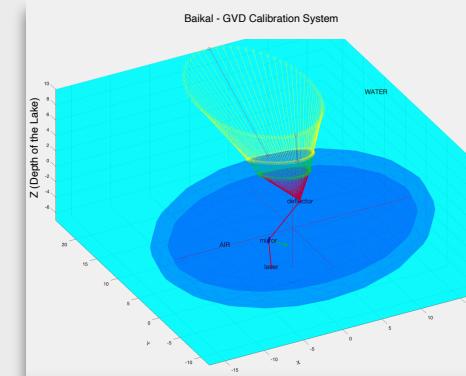
Monte Carlo and detector data compatibility check for charged particle production in Xe-Xe collisions.



Hijing 1.38b simulation vs ATLAS 2017 data; pixel dead sensor differences require correction.

For more: [Presentation](#)

Baikal Gigatonne Volume Detector (GVD) Underwater Alignment Studies

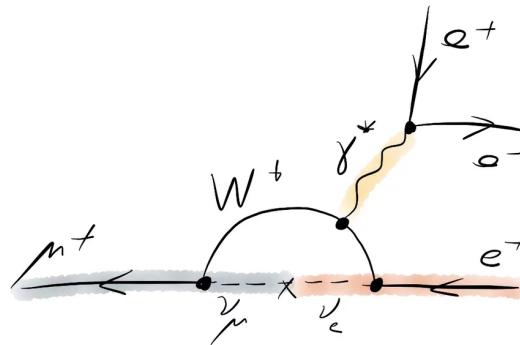


Laser-based calibration system for the Baikal-GVD. The detector's position (ellipsoid) is determined from the Cherenkov cone angle. Refraction effects are visualized using Snell's law.

Scientific Assistant in Mu3e Experiment

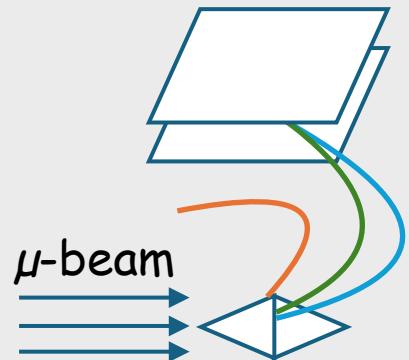


Search for a lepton flavor violation $\mu^+ \rightarrow e^+ e^- e^+$



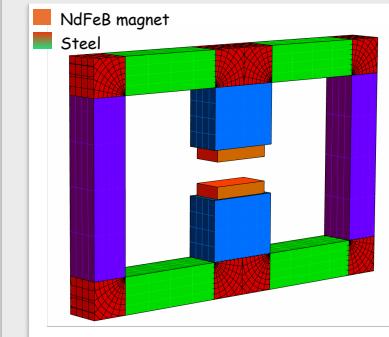
Impact of Increasing Magnetic Field:

Simulated over 10^7 events in  GEANT4.

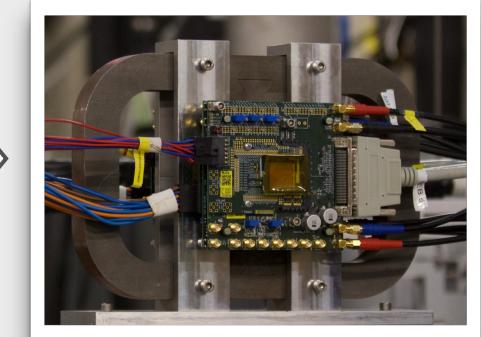


Mass Resolution **improves**
Efficiency **decreases**
Trade-Off

Magnetic Field Simulation of the Spectrometer:

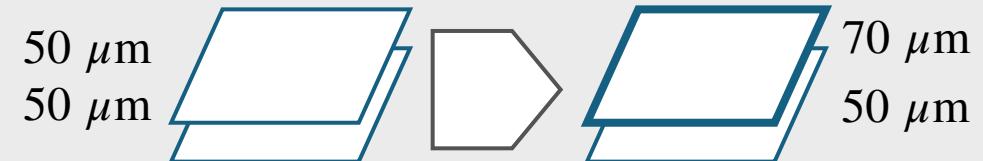


Radia Simulation



MuPix11 Testing

Impact of Increasing Sensor Thickness:



► Mass Resolution **decreases**
► Efficiency **decreases**

PSI Practical Course

ETHzürich



UNIVERSITÄT
HEIDELBERG
ZUKUNFT
SEIT 1386

JG|U

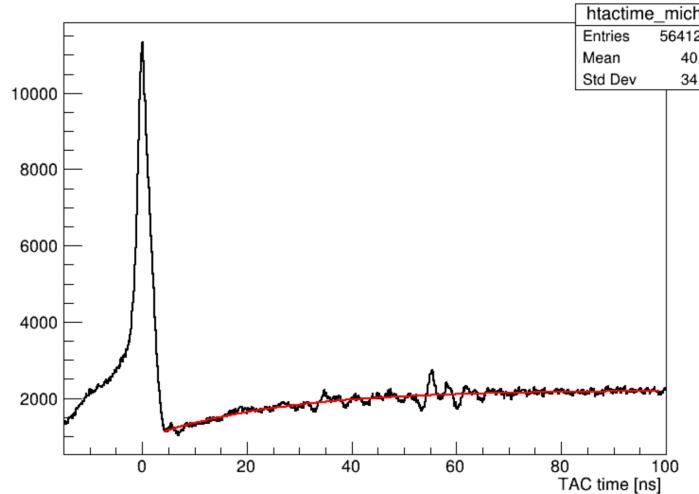
Pion life-time measurement:

- Constant λ_μ and background

Bateman Equation:

$$N(t) = N_0 \frac{\lambda_\pi}{\lambda_\mu - \lambda_\pi} \left(e^{-\lambda_\pi t} - e^{-\lambda_\mu t} \right) + \text{Background}$$

TAC time, michel



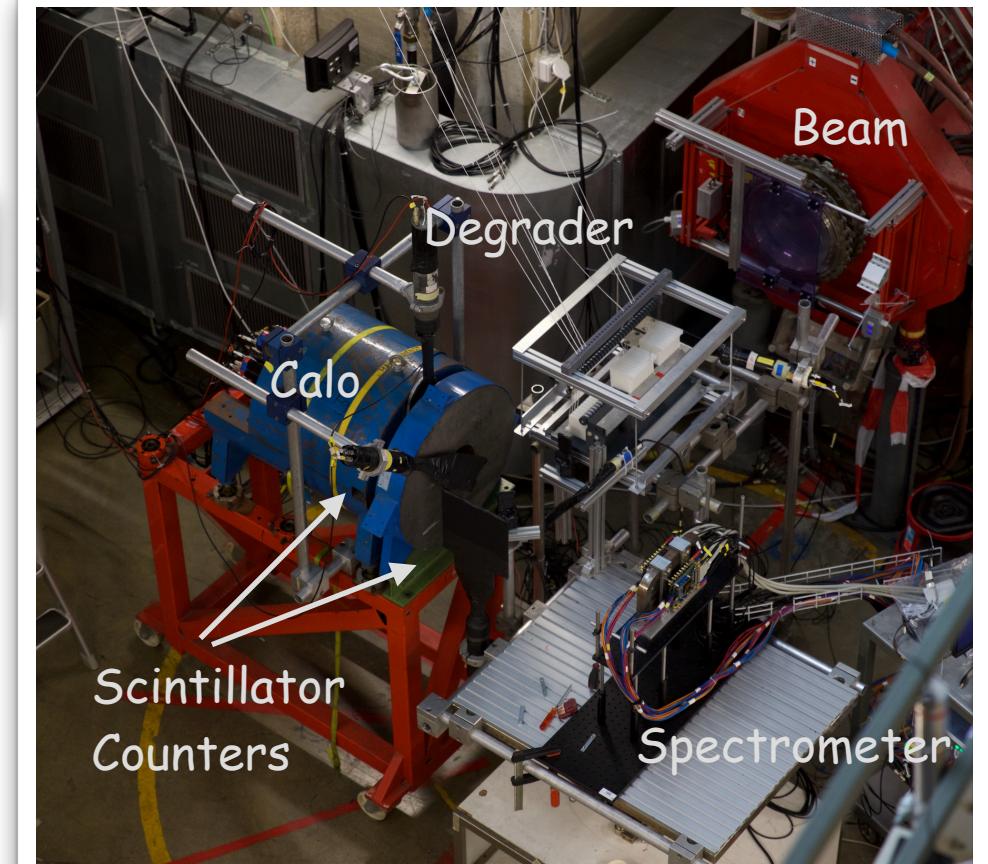
$N(t)$ from a parent (λ_π) decaying into a daughter (λ_μ), including background.

Calo analysis yields:

$$\tau_\pi = 26.01 \pm 0.71(\text{syst.}) \pm 0.35(\text{stat.}) \text{ ns}$$

PDG Value:

$$\tau_\pi = 26.0033 \pm 0.0005 \text{ ns}$$



Note: The same spectrometer is used to measure the pion lifetime.

Part-time

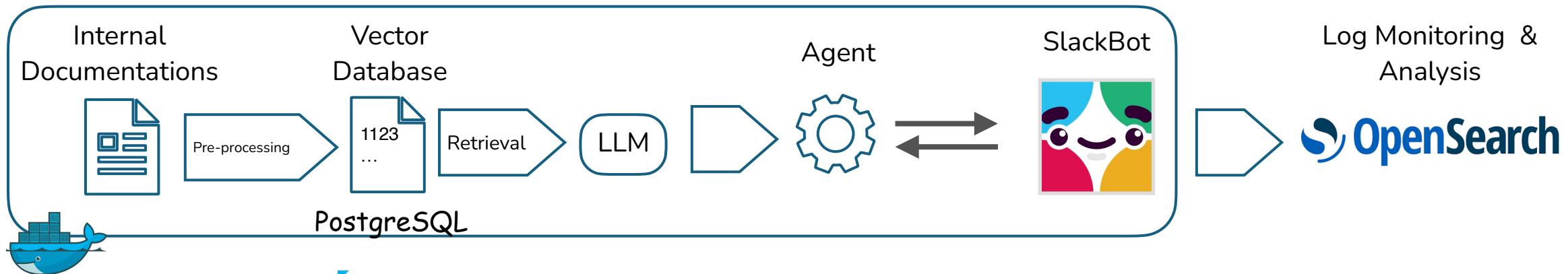
Industry Experience at (2+ years)

- Machine Learning Engineering in Business and Technology Platform



CLOUD FOUNDRY

Developing
Agentic RAG Prototype



- Big Data Analyst in 

Organizational
Tasks



Data
Visualization



Prepare Weekly
Reports & Presentations



Wiki & Jira
Maintenance

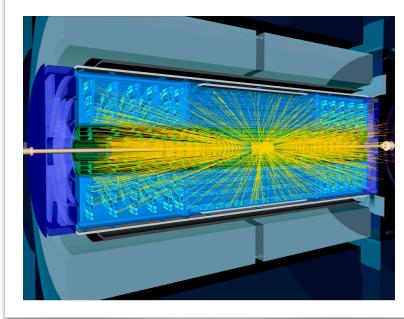


Judge at Finalist
Selection



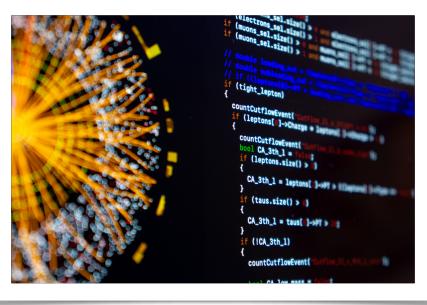
Master's Thesis: HLS-Based FPGA Implementation of the General Triplet Track Fit (GTTF) Algorithm for Real-time Particle Tracking

Pile-up Challenge



- Combinatorial explosion
- Up to 200 collisions per bunch crossing
- Real-time processing constraints

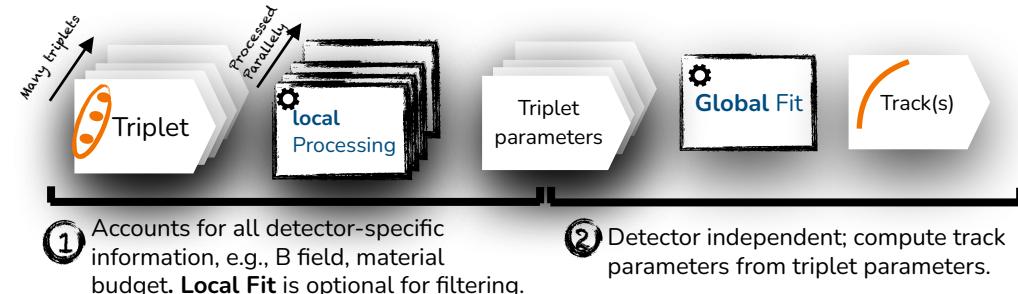
Computational Load



A novel track fitting algorithm!

Solution

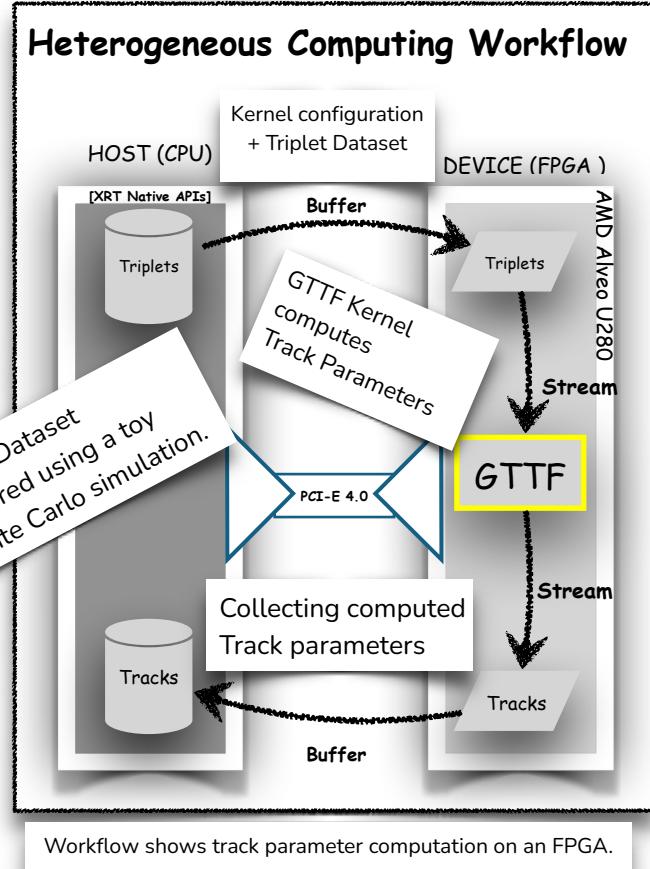
Triplet track fit based on two-step procedure that can be factorized:



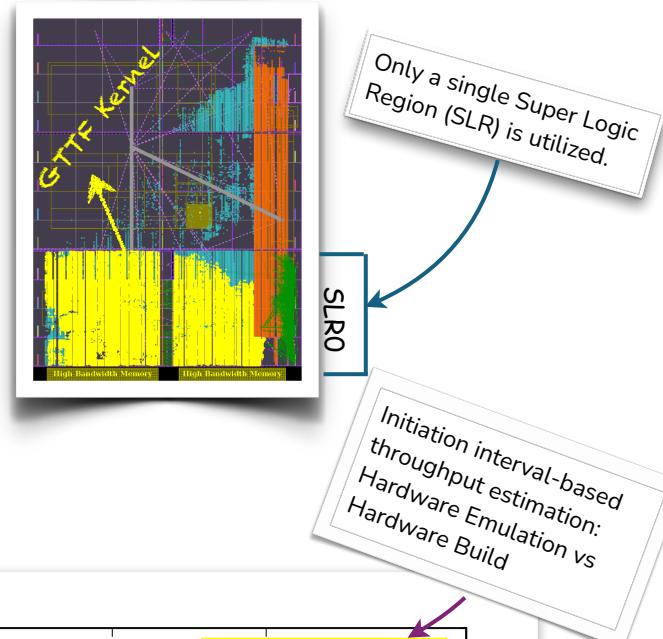
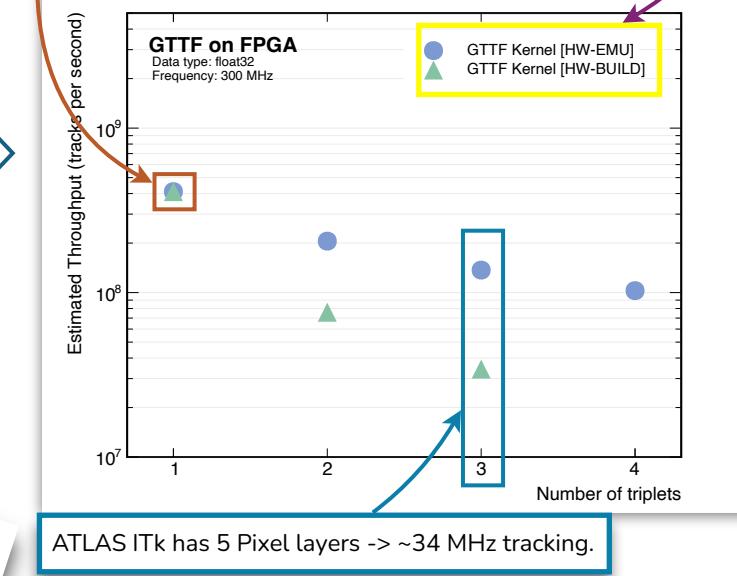
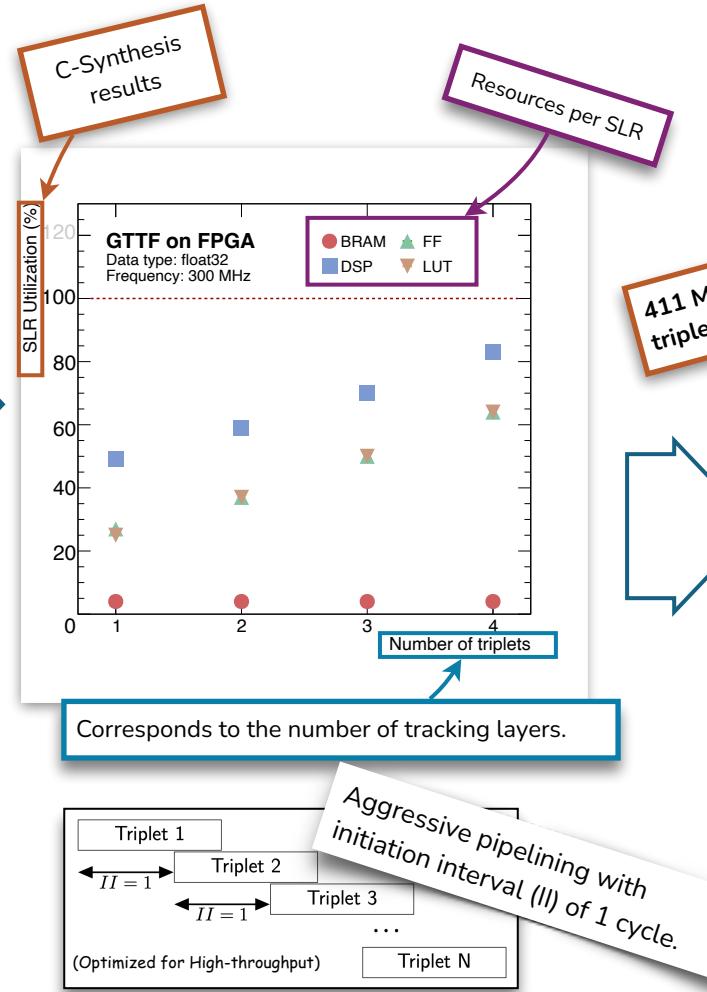
Poster available:

• K. Tastepe, S. Dittmeier, A. Nandi, C. Sauer, and A. Schöning, *FPGA Implementation of the General Triplet Track Fit*, EPJ Web Conf., vol. 337, p. 01198, 2025.

Master's Thesis: HLS-Based FPGA Implementation of the GTTF Algorithm for Real-time Particle Tracking



Link to repository: [hls_triplet_fit](https://github.com/kadir-tastepe/hls_triplet_fit)

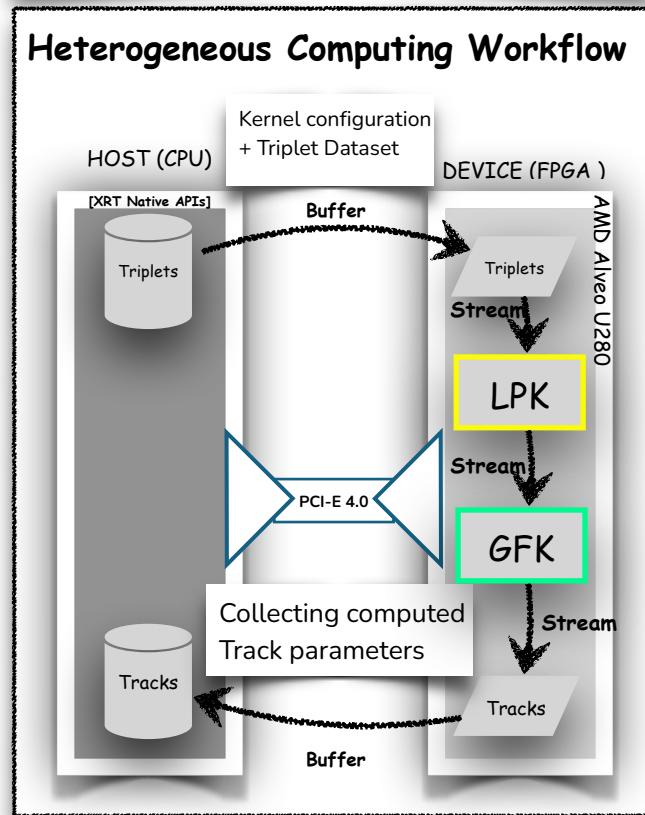


Master's Thesis: HLS-Based FPGA Implementation of the GTTF Algorithm for Real-time Particle Tracking

The GTTF is decomposed into two kernels.

$$\text{GTTF Kernel} = \text{Local Processing Kernel (LPK)} + \text{Global Fit Kernel (GFK)}$$

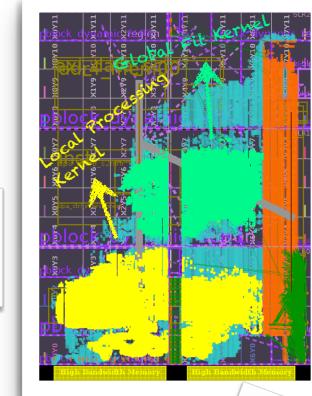
(Compute Tracks) (Processing Triplets) (Combining Triplets)



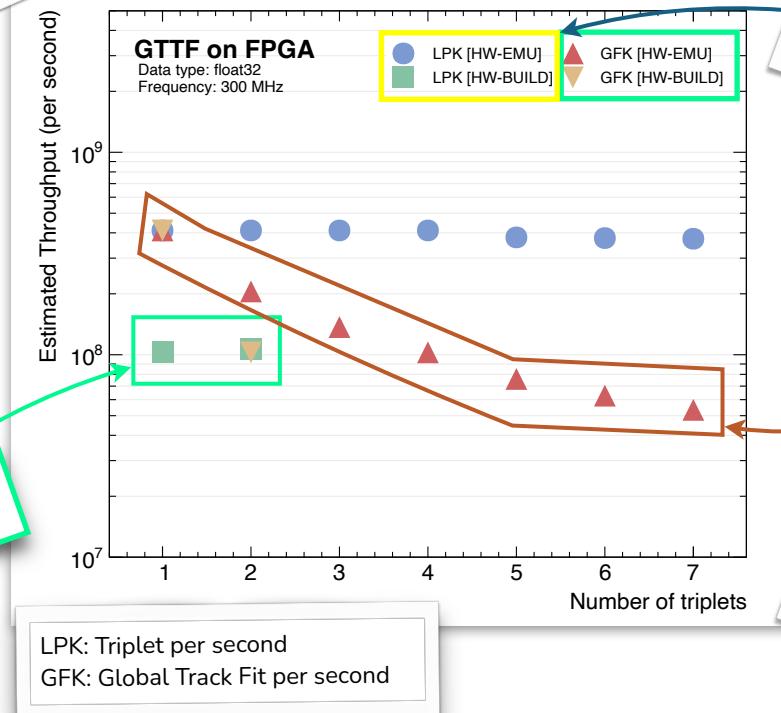
Clock frequency, initiation intervals, and data types have to be defined manually to guide the synthesis.

As the design gets more complex, HLS struggles to meet timing requirements.

Global Fit is the bottleneck for the design.



Initiation interval-based throughput estimation: Hardware Emulation vs Hardware Build



Supports up to 7 triplets.

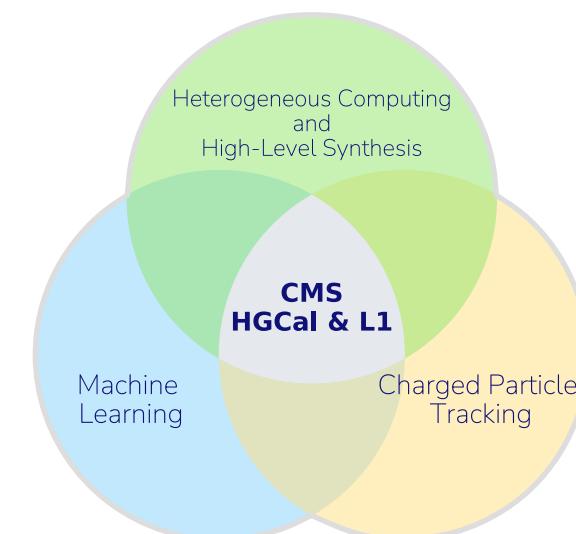
Multi-kernel design. Each is assigned to a different SLR.

Data acquisition, trigger and online reconstruction for the CMS High Granularity Calorimeter and the L1 Track Trigger

- As the HL-LHC will be the last hadron-hadron collider operating at comparable energies for many years, it is essential to maximize its potential by optimizing the trigger strategy!
- Developing faster, smarter, and more efficient trigger and data acquisition systems of CMS in HL-LHC to identify rare events (such as exotic particles, long-lived particles, etc.).
- Including cutting-edge detector technologies such as machine learning, novel algorithms, and heterogeneous computing architectures to improve the performance of trigger systems.

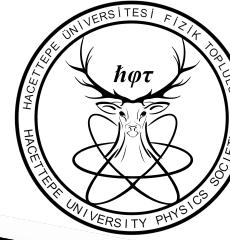


NGT



The intersection of my skills and the project's needs is exactly where I can make a difference.

Scientific Outreach Activities



Hacettepe University Physics Society
• Founding Member

Together with my team, we have organized many conferences, seminars, and scientific excursions. (2017-2022)



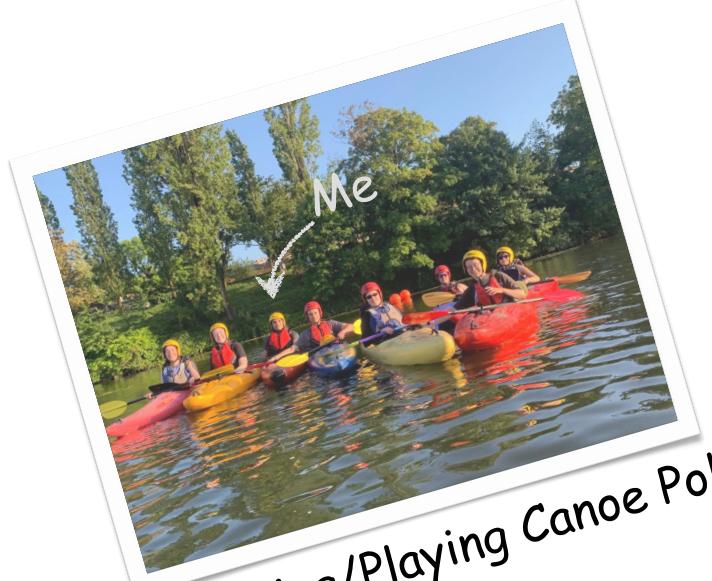
Voluntary Science Communication

- Continues to thrive today <https://www.instagram.com/hacettepefiziktopluluğu/>



Interdisciplinary Da Vinci Symposium.
(200+ participants, including the rector)

Extracurricular Activities



Kayaking/Playing Canoe Polo



Birdwatching



Winter Expedition



Hiking



Stand-Up Paddling

Thank you very much for
your attention!

Feel free to ask any questions.

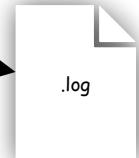
Backup

User Interface

Input



Output



X

What is High Level Synthesis?

- From this  to this  to run on this .

In other words: HLS translates high-level languages (C, C++) to FPGA

This is C/C++ code with some compiler statements to tell HLS what to do.

Source Code

This is just for debugging the C/C++ code using HLS libraries. Compiling:
FAST ~ O(seconds)

This is for emulating the synthesised code on target hardware. Compiling: **NOT SO FAST ~ O(minutes)**

Software Emulation

Hardware Emulation

If everything works, build for actual hardware. Compiling: **VERY SLOW ~ O(hours)**

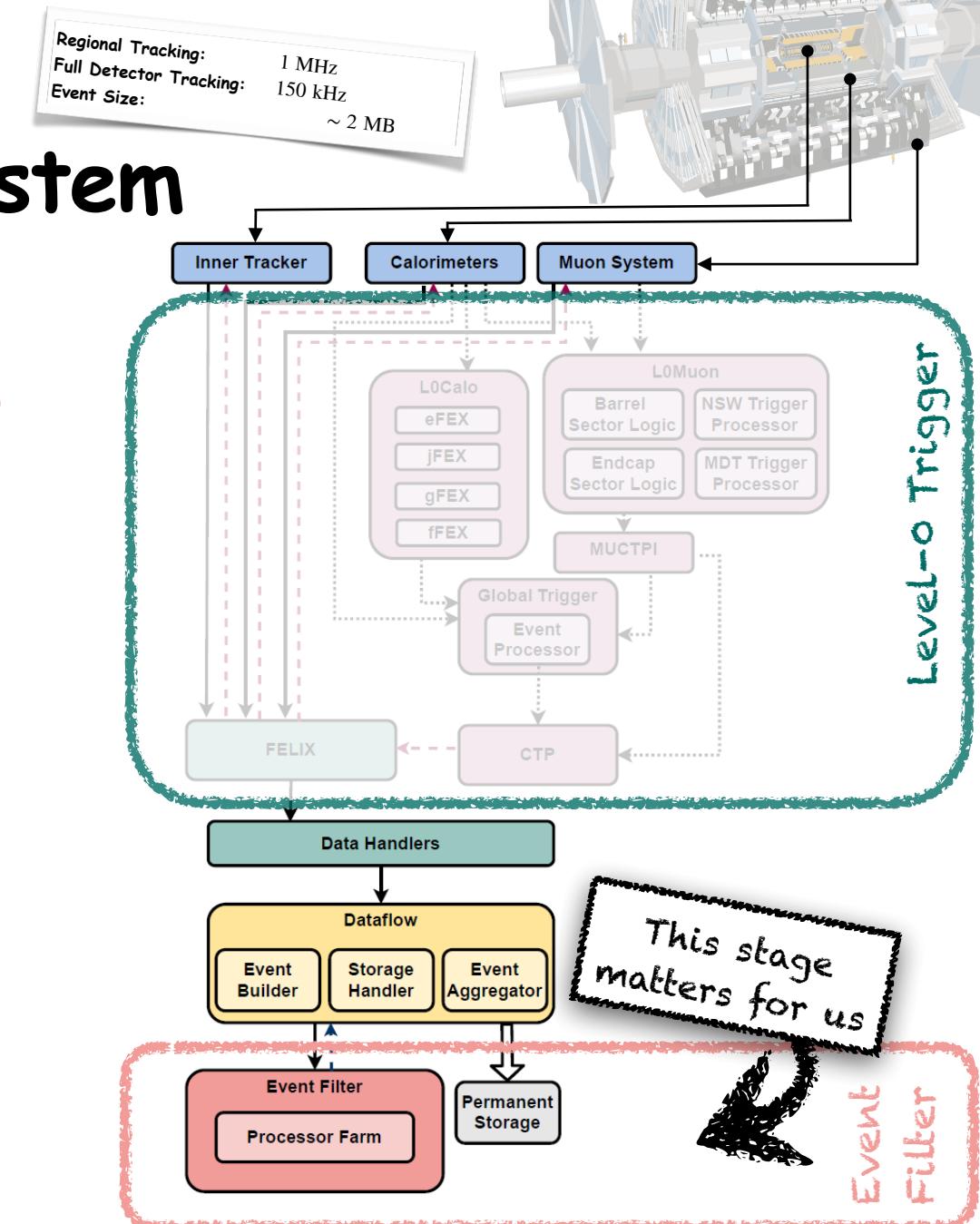
Hardware

The ATLAS Trigger & Data AcQuisition (TDAQ) System

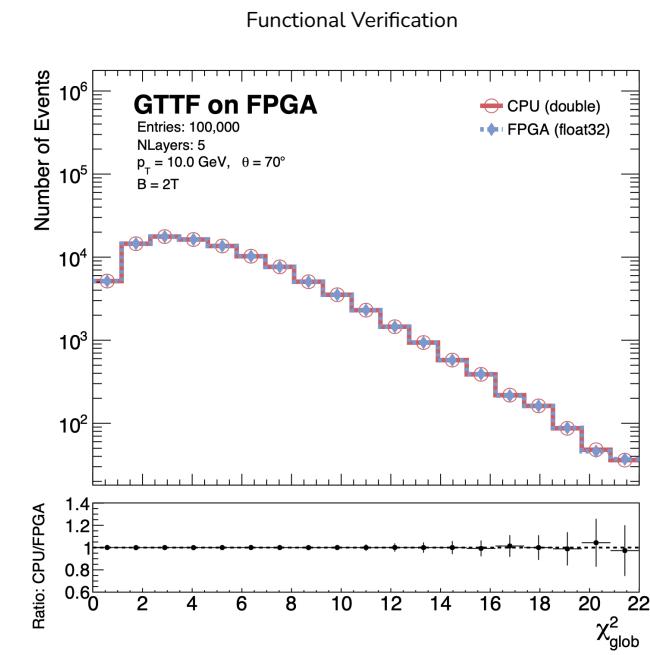
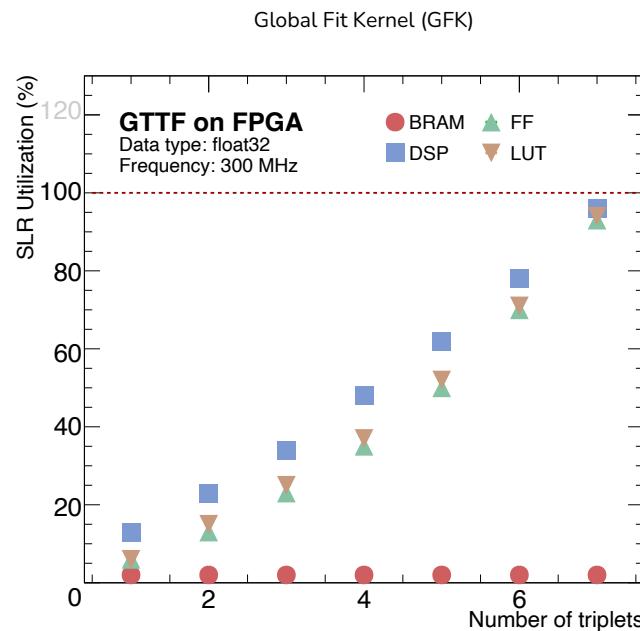
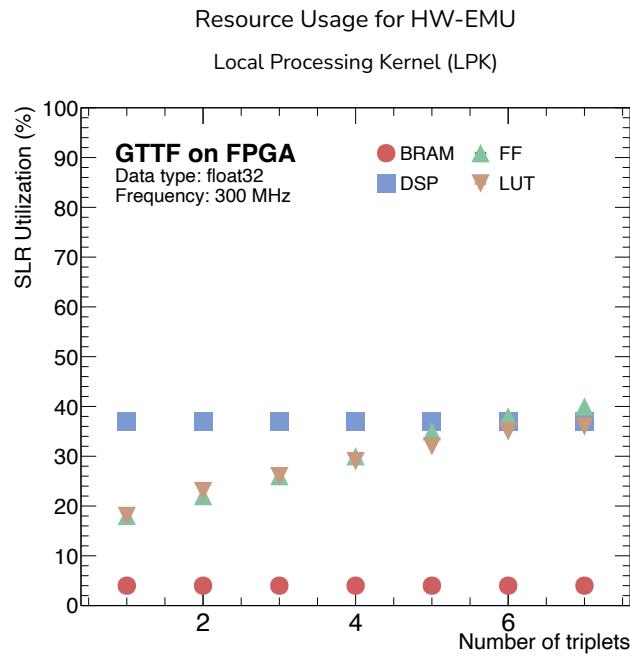
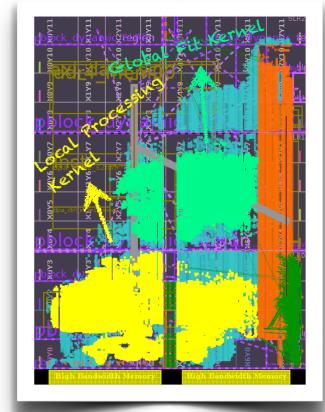
- 2 Level Trigger System **Level 0** and **Event Filter** to select interesting events
- **Event Filter** will be based on Heterogeneous computing farm
- Current ongoing competition of ideas for future tracking concepts



(General) Triplet Track Fit



Master's Thesis: HLS-Based FPGA Implementation of the GTTF Algorithm for Real-time Particle Tracking



Floorplan of Kernels

Access Global Memory

Read From Global Memory

Triplet Parameters

Hit Gradients & Local Fit

Compute Global Fit

Write to Global Memory

Access Global Memory

(6)

Hit Gradients + Local Processing

(5)

Streaming calculated parameters to Local "Fit" Function

(4)

Triplet Parameter Calculation

(3)

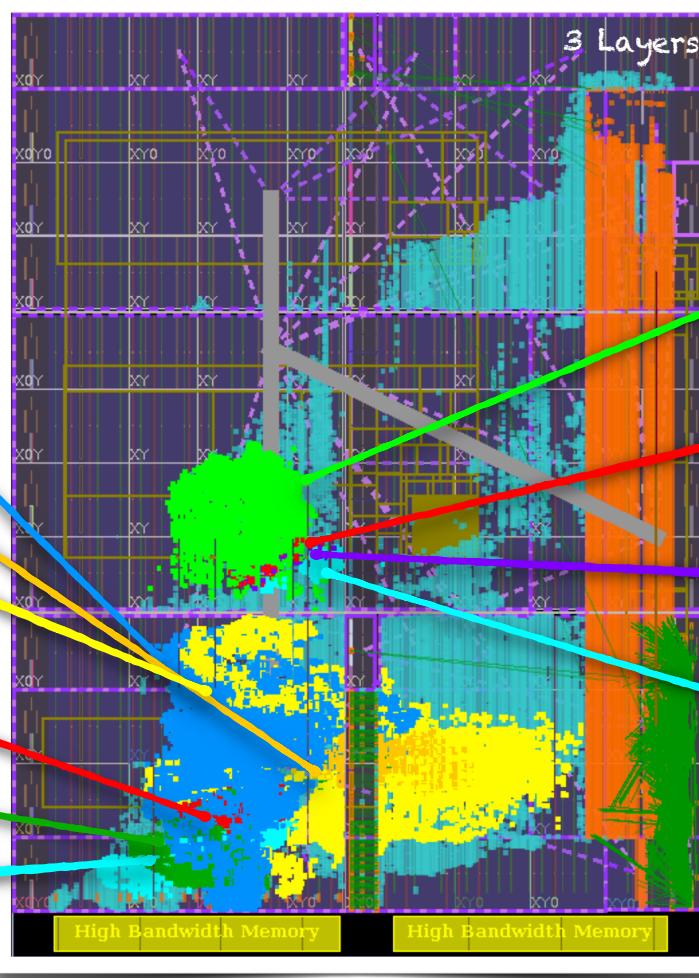
Streaming Triplets to Triplet Parameters function.

(2)

Reading Triplets from the Global Memory

(1)

Using m_axi port to access Global Memory.



Global Fit

Streaming Track Parameters to Write Global Memory

Writing Track Parameters to Global Memory

Using m_axi port to access Global Memory

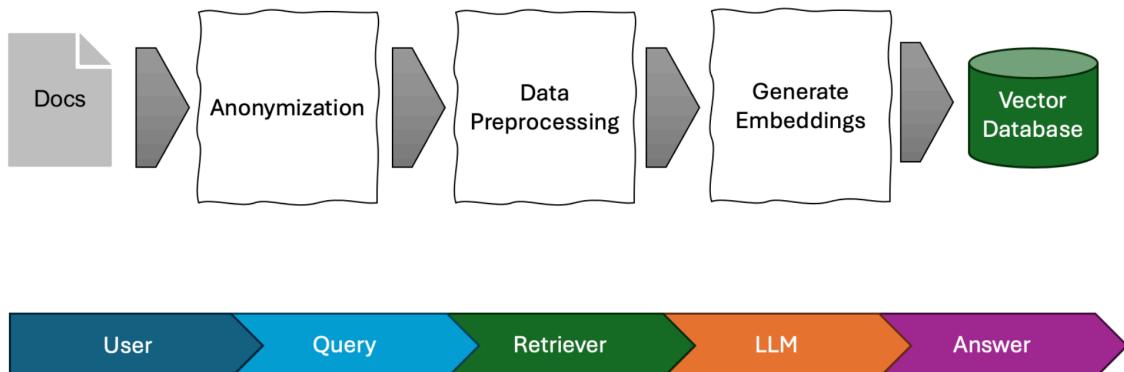
(7)

(8)

(9)

(10)

RAG (Retrieval Augmented Generation)



- Identify and resolve common software issues faster. (Accelerate **RCA**)
- Prevent re-investigating known issues, **saving hours of employee time**.
- **Context aware** troubleshooting.
- Track frequently asked questions and suggest missing documentation.
- Keep internal knowledge bases fresh, allows **rapid database change**. (~ 2 MB per minute)
- Effective **knowledge transfer**, document summarization.
- Boost **developer onboarding**.
- Enable cross-team collaboration.
- The same project can be used by other teams.

Anonymized RAG Pipeline

